



THEORETICAL FLAT AREA  
DEVELOPED FLAT AREA

GENERAL TOLERANCES  
ANGLES : ±°  
LENGTHS AND DIAMETERS  
18-80 mm : ±0.1  
>80 mm : ±0.2

[2] CRITICAL DIMENSIONS WHICH MUST BE UNDER CAPABILITY

SAFETY RISKS  
VISIBLE SURFACES  
DRAFT ANGLES (NOT SHOWN): 0°/00'00"

A4

D	drawing modified for standardization	Georgi	DGT 15936	03-03-06													
C	position of thermal switch changed	Wiederer	DGT 6917	00-12-06													
B	pin names of power supply clamps adapted	Käser	DGT 4952	00-07-18													
A	new drawing created		DGT 2311	99-11-25													
ZUST	MODIFICATIONS		SIGNATURE	REQUEST	DATE												
DESIGN	OWNER	DRN	CHD	DATE	DESIGN	USERS	EUROPEAN METHOD										
DGT	DGR-PE_PD	Käser	TAPPR	99-11-25			⚡										
<table border="1"> <tr> <td>TITLE</td> <td colspan="2">circuit diagram oven</td> </tr> <tr> <td>TITEL</td> <td colspan="2">Schaltplan Herd</td> </tr> <tr> <td>NUMBER</td> <td>330 2705</td> <td>SHEET 01 (01)</td> </tr> <tr> <td>REV</td> <td>D</td> <td></td> </tr> </table>						TITLE	circuit diagram oven		TITEL	Schaltplan Herd		NUMBER	330 2705	SHEET 01 (01)	REV	D	
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